

REASONS FOR ALLOWANCE

1. Claims 1-3, 5-7, 11, 12, and 14-20 are allowed over the prior art of record.
2. Claims 4, 8-10, and 13 have been canceled.
3. The following is an examiner's statement of reasons for allowance: Upon further consideration and updated search, the applicant's arguments filed on 11/6/2007 are persuasive. **The feature of independent claim 1** directed towards allowable subject matter is a scan driving circuit for simultaneously applying scan pulses to both ends of each scan line of the flat display panel, wherein the scan pulses have a same voltage, a same phase and a same pulse width. **The feature of independent claim 5** directed towards allowable subject matter is a first scan driving circuit for applying scan pulses to one side of each scan line of the flat display panel, and a second scan driving circuit for applying the scan pulses to the other side of each scan line, wherein the scan pulses have a same voltage, a same phase and a same pulse width. **The feature of independent claim 11** directed towards allowable subject matter is simultaneously applying scan pulses to both ends of each scan line of the flat. display panel, wherein the scan pulses have a same voltage, a same phase and a same pulse width. **Kinoshita in view of Johnson et al. teaches of** a like display device, but fails to teach of the above claimed features that requires scan pulses have a same voltage, a same phase and a same pulse width. **Therefore** the above said features in combination with the other limitations of the above independent claims 1, 5, and 11, and therefore claims 1-3, 5-7, 11, 12, and 14-20, are found to be allowable over the prior art of record.
4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should

preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is **(571) 272-7673**. The examiner can normally be reached on MTWTHF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on **(571) 272-7681**. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571)-273-8300.
6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner: David L. Lewis

April 14, 2008

/David L Lewis/

Primary Examiner, Art Unit 2629